

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Tsuyoshi YAMAMOTO, et al.

Serial No.: Not Yet Assigned

Filed: February 12, 2002

For: **METHOD OF MOUNTING ELECTRONIC COMPONENT ON SUBSTRATE
WITHOUT GENERATION OF VOIDS IN BONDING MATERIAL**

#3/POX
9/20/02
8/19/02



INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

February 12, 2002

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

Copies of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

Deposit Account No. 01-2340.

Respectfully submitted,
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Enclosures: PTO-1449; References (2)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 020154	Serial No. New Appln.
	Applicant(s): Tsuyoshi YAMAMOTO, et al.	
	Filing Date: February 12, 2002	Group Art Unit:

30978 U.S. PTO
 10/073106
 02/12/02

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA	5,878,942	Kodama et al.	03/09/99			
_____	AB						
_____	AC						
_____	AD						
_____	AE						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
_____	AF	9-92682	04/04/97	Japan	Abstract
_____	AG				
_____	AH				
_____	AI				
_____	AJ				

OTHER DOCUMENTS

_____	AK			
_____	AL			
<table border="1" style="width: 100%;"> <tr> <td style="width: 50%;">Examiner</td> <td style="width: 50%;">Date Considered</td> </tr> </table>			Examiner	Date Considered
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